Modifications to Energia Files to compile 433 MhZ CC1350 Launchpad

Initialize the RF switch in the Board.h and Board\_init.h files located at

* ‎Macintosh\_HD/Users/ryanshipley/Library/Energia15/packages/energia/hardware/cc13xx/4.9.1/variants/LAUNCHXL\_CC1350

[CC1350](http://www.ti.com/product/CC1350)\_LAUNCHXL\_DIO1\_RFSW | PIN\_GPIO\_OUTPUT\_EN | PIN\_GPIO\_HIGH | PIN\_PUSHPULL | PIN\_DRVSTR\_MAX, /\* RF SW Switch defaults to sub-1GHz path\*/  
[CC1350](http://www.ti.com/product/CC1350)\_LAUNCHXL\_DIO30\_SWPWR | PIN\_GPIO\_OUTPUT\_EN | PIN\_GPIO\_HIGH | PIN\_PUSHPULL | PIN\_DRVSTR\_MAX, /\* External RF Switch is powered on by default \*/

by adding the following to the Boards\_init.c file (in green)

const PIN\_Config BoardGpioInitTable[] = {

Board\_RLED | PIN\_GPIO\_OUTPUT\_EN | PIN\_GPIO\_LOW | PIN\_PUSHPULL | PIN\_DRVSTR\_MAX, /\* LED initially off \*/

Board\_GLED | PIN\_GPIO\_OUTPUT\_EN | PIN\_GPIO\_LOW | PIN\_PUSHPULL | PIN\_DRVSTR\_MAX, /\* LED initially off \*/

Board\_BTN1 | PIN\_INPUT\_EN | PIN\_PULLUP | PIN\_IRQ\_BOTHEDGES | PIN\_HYSTERESIS, /\* Button is active low \*/

Board\_BTN2 | PIN\_INPUT\_EN | PIN\_PULLUP | PIN\_IRQ\_BOTHEDGES | PIN\_HYSTERESIS, /\* Button is active low \*/

Board\_UART\_RX | PIN\_INPUT\_EN | PIN\_PULLDOWN, /\* UART RX via debugger back channel \*/

Board\_UART\_TX | PIN\_GPIO\_OUTPUT\_EN | PIN\_GPIO\_HIGH | PIN\_PUSHPULL, /\* UART TX via debugger back channel \*/

Board\_SPI0\_MOSI | PIN\_INPUT\_EN | PIN\_PULLDOWN, /\* SPI master out - slave in \*/

Board\_SPI0\_MISO | PIN\_INPUT\_EN | PIN\_PULLDOWN, /\* SPI master in - slave out \*/

Board\_SPI0\_CLK | PIN\_INPUT\_EN | PIN\_PULLDOWN,

CC1350\_LAUNCHXL\_DIO1\_ANALOG | PIN\_GPIO\_OUTPUT\_EN | PIN\_GPIO\_HIGH | PIN\_PUSHPULL | PIN\_DRVSTR\_MAX, /\* RF SW Switch defaults to sub-1GHz path\*/

CC1350\_LAUNCHXL\_DIO30\_ANALOG | PIN\_GPIO\_OUTPUT\_EN | PIN\_GPIO\_HIGH | PIN\_PUSHPULL | PIN\_DRVSTR\_MAX, /\* External RF Switch is powered on by default \*/ /\* SPI clock \*/

PIN\_TERMINATE

}

and the following to the Board.h file under the pin mapping –

/\* Radio Switch Pins \*/

#define CC1350\_LAUNCHXL\_DIO1\_ANALOG IOID\_1

I also modified the EasyLink files in the ‘EasyLink’ library for 433 MhZ.

Describe those changes here.

Also need to add

CCFG\_FORCE\_VDDR\_HH = 1 in the ccfg.c file.

to program for 14 dB output power